

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Canada

75,520 A. J. Kerklaan and M. J. Preyde A PCMCIA Optical Transceiver

France

132,818 J. Martin Alemany, J. Sevilla Perez, and M. L. Mocholi Interactive Service Terminal
 320,555 A. Brunin, B. Caplier, H. Berander, and J. P. Rousseau Extended Cascode Logic Family
 339,165 P. Mollier and P. Tannhof Push-Pull Source Coupled FET Logic
 343,305 J. Fieschi, J. F. Le Pennec, M. Glisse, and P. E. Le Specialized Processor for ISDN Bit Rate Adapter
 364,647 C. Galand, J. Menez, and M. Rosso Adaptive Code Excited Linear Predictive Coder (ACELP)
 374,338 A. Pauporte, C. Poiraud, C. L. Basso, G. Lebizay, and J. M. Munier Shared Intelligence Memory for the Interconnection of Distributed Micro Processors
 418,447 B. Naudin, J. M. Millet, and J. P. Lips Device for Controlling the Enqueuing and Dequeuing Operations of Messages in a Memory
 456,947 D. Orsatti, E. Saint-Georges, F. Nicolas, F. Verplanken, G. Toubol, and J. Calvignac Hybrid Switching System for a Communication Node
 470,320 B. Naudin, C. Beaulieu, J. Calvignac, and J. M. Millet Mechanism for Performing the Steady State and Error Recovery Functions of a Communication Protocol
 472,797 C. Jacquart, M. Ferry, P. Vachee, and Y. Fermier Overload Protection Circuit
 520,115 A. Paci, C. L. Lasmayoux, H. Braquet, M. Ferry, and P. Vachee Flexible Capacitor

Germany

68,922,738.8 R. Clemen and W. Loehlein Highly Integrated Multi-port Semiconductor Storage
 69,016,785.7 A. Blum, F. Gerth, M. Perske, and M. Schmidt Removable VLSI Package
 69,019,822.1 G. Zilles, H. Kohler, I. Holm, N. Schumacher, and P. Mannherz Array Addressing Check Mechanism
 69,020,155.9 D. Weiland, D. F. Wendel, and J. Riegler Method and Apparatus for Testing a VLSI Device
 4,221,575.7 G. Koetzle, H. Schettler, T. Ludwig, and V. Kreuter CMOS Integrated Semiconductor Circuit
 4,334,294.9 H. Chilinski, K. J. Getzlaff, R. Koester, and W. Haller String Processor

4,342,521.6	H. Kohler, K. J. Getzlaff, N. Schumacher, R. Koester, T. Stoehr, and T. G. Koehler	Method for the Expansion of Compressed Data
9,409,768.2	W. Guber	Home Banking Terminal
9,409,717.8	W. Guber and W. Weber	Service Terminal for Banking Purposes
9,409,927.8	V. Boettinger and W. Guber	Enclosure for a Brightness Sensor
9,409,928.6	E. Hindermeyer, J. Gall, J. Moll, J. Schnee, K. Staudemaier, L. U. Fischer, M. Hoeschele, P. Schmidt, S. G. Feger, and Abendschein	Printer
9,409,929.4	V. Boettinger and W. Guber	Power Pack
9,409,995.2	W. Guber	Device for Home Control System
9,500,247.2	P. Rudolph and W. Guber	Speaker Phone
3,650,323,108	H. Schettler, K. Klein, K. Pollmann, O. Wagner, O. M. Wagner, R. Zuehlke, and U. Schulz	Method for Physical VLSI-chip Design
3,853,476,208	D. W. Bock, H. Schulze-Schoelling, K. Grassmann, and P. Rudolph	Arrangement for Error Recovery in a Self-guarding Data Processing System
Japan		
922,266	T. Imai	Flat Display for Computer
1,896,714	H. Masuda, K. Shimada, and S. Kawabe	A Fast Reshaping Method for B-rep Solid Models
1,902,442	N. Ohba and S. Shimizu	Adaptive Consistency Control Mechanism
1,903,532	T. Fujino	Semi-automatic Document Feeder
1,906,392	N. Nishiimura	Speaker Adaptation Method for VQ Code Book
1,906,418	T. Murakami	Double Use of Photo Decompose Type Resist on Multi-layered Circuit
1,920,767	K. Sekiya and Y. Shiraishi	A Simple Real-time Halftoning Method for 64-Level Gray Image
1,920,815	T. Matsumoto	An Efficient Synchronization Mechanism for Existing OSS
1,922,397	F. C. Canova	Text Recovery in Color Compressed Video
1,926,228	H. Tsujino, S. Y. Yasufuku Seiichi, and T. T. Takagi	Connector Fixed Space Saving FPC ASM
1,926,229	H. Tsujino and T. T. Takagi	Concurrent Spindle Connector
1,926,230	J. Kimura and T. T. Takagi	Card/DE Assembly Connector
1,929,054	S. Matsubara, T. Mori, and T. Tabata	Area Shading for Color Graphic Terminal
1,931,907	T. Suzuki, Y. Honda, and Y. U. Honda	Auto Gap Adjust in Printer
1,931,929	H. Nishimura	Bump Build by Solder Injection
1,934,683	H. Hama and Y. Isobe	Method for Displaying an Image
1,934,785	A. Yamashita and H. Takahashi	A Character Recognition Algorithm
1,938,740	H. Kitahori, K. Takada, and Y. Kakizaki	Self-align Cover Seal
1,940,610	F. Ohsawa, I. Koyanagi, M. Ohtsuki, and T. Satoh	Mounting Method for HDD Package
1,945,979	K. Mitsui	An Efficient Quantitative Retrieval Mechanism Based on an Inverted File
1,940,586	M. Fukuda, T. Matsumoto, and T. Nakata	Light Synchronization Mechanics with Bus
Switzerland		
91,220	D. S. Chevion, E. Walach, and E. D. Karnin	Efficient Compression and Decompression of Forms
91,221	D. S. Chevion, E. Walach, and E. K. Karnin	Method for High Quality Compression of Binary Text Images
United Kingdom		
394,572	C. N. Wallis	Data Storage Device
417,110	A. R. Hearn, C. M. Lacey, and M. R. Hatchett	Method of Assembling a Disk File
422,296	D. C. Haigh and R. W. Farr	Display System with Direct Colour Mode
463,250	J. Kane and M. Doyle	Data Processing Apparatus User Interface
2,042,505	M. D. Oates	Visual Display Unit
2,043,155	M. H. Sharp, P. Clark, and W. L. Aderman	Data Storage Unit
2,043,156	M. H. Sharp, P. Clark, and W. L. Aderman	Data Storage Unit
2,043,211	D. J. Gray, J. D. Haley, and P. Clark	Data Processing Unit

2,043,297	M. H. Sharp	Data Storage Unit
United States		
5,388,244	G. A. Brent and C. A. Scalzi	Controls for Initial Diversion of Page-Frame Logical Content as Part of Dynamic Virtual-to-Real Translation of a Virtual Page Address
5,388,993	F. A. McKeil, G. M. Trowbridge, C. J. Cavendish, and R. J. Catino	Method of and System for Demonstrating a Computer Program
5,394,382	L. Hu and S. Ong	Method for the Organization of Data on a CD-ROM
5,395,650	M. M. B. Holl, S. P. Kowalczyk, F. R. McFeely, and P. F. Seidler	Selective, Low-Temperature Chemical Vapor Deposition of Gold
5,395,769	M. Arienzo, D. L. Harame, and G. S. Oehrlein	Method for Controlling Silicon Etch Depth
5,396,383	T. A. Gregory and C. G. Keller	Integral Lubricating Fluid Delivery System for a Flying Head in a Magnetic Disk Storage System
5,396,455	M. J. Brady, R. J. Gambino, L. Krusin-Elbaum, and R. R. Ruf	Magnetic Non-Volatile Random Access Memory
5,396,602	N. Amini, P. M. Bland, B. F. Boury, R. G. Hofmann, and T. J. Lohman	Arbitration Logic for Multiple Bus Computer System
5,397,240	J. D. Herard	Electrical Connector
5,397,598	N. DiPaolo, B. Ghosal, and K. H. Ruffing	Method for Selectively Coating a Member Having a Shank by Masking a Portion of the Shank with a Washer
5,397,604	R. P. Phillips III, N. J. Dauerer, P. Elenius, B. M. Kerrigan, H. Krueger, and R. O. Lussow	Non-Contact Fluid Applicator Apparatus and Method
5,397,607	R. R. Shields, K. A. Smith, and R. J. McDonald	Input/Output (I/O) Thin Film Repair Process
5,397,684	D. P. Hogan, H. G. Linde, and R. A. Warren	Antireflective Polyimide Dielectric for Photolithography
5,397,323	R. H. Taylor, J. Funda, D. D. Grossman, J. P. Karidis, and D. A. LaRose	Remote Center-of-Motion Robot for Surgery
5,397,741	L. J. O'Connor, R. A. Previti-Kelly, and T. J. Reen	Process for Metallized Vias in Polyimide
5,397,747	J. M. Angiulli, E. S. Kolankowsky, R. R. Konian, and L. L. Wu	Vertical Chip Mount Memory Package and Method
5,397,863	A. Afzali-Ardakani, J. Ayala-Esquilin, B. E. Braren, S. Daijavad, E. Foster, J. L. Hedrick, Jr., J. C. Hedrick, R. T. Hodgson, A. A. Mehta, S. E. Molis, J. M. Shaw, S. L. Tisdale, and A. Viehbeck	Fluorinated Carbon Polymer Composites
5,397,957	S. M. Zimmerman	Process and Structure of an Integrated Vacuum Microelectronic Device
5,398,012	J. H. Derby, W. A. Doeringer, J. E. Drake, Jr., D. H. Dykeman, H. J. Sandick, and K. V. Vu	Distributed Processing of Route Selection Across Networks and Subnetworks
5,398,164	W. A. Goodman and W. F. Ott III	Printed Circuit Card Latching and Stiffening Assembly
5,398,195	M. Y. Kim	Method and System for Providing a Non-Rectangular Floor Plan
5,398,312	R. P. Hoffman	Method of Creating, Defining, or Editing a Graphical User Interface Panel File
5,398,321	T. L. Jeremiah	Microcode Generation for a Scalable Compound Instruction Set Machine
5,398,331	K. C. Huang and D. A. Wise	Shared Storage Controller for Dual Copy Shared Data
5,399,101	J. S. Campbell and T. G. Macek	Electrical Connector with Preloaded Contact
5,399,305	A. Bross and T. Walsh	Dynamic Gating of Polymers For Isotropic Properties
5,399,386	C. V. Jahnes, M. T. Mirzamaani, and M. A. Russak	In-Situ Texturing of a Thin Film Magnetic Medium
5,399,462	K. G. Sachdev, P. Jagannathan, R. N. Lang, H. S. Sachdev, R. Sooriyakumaran, and J. R. Whitaker	Method of Forming Sub-Half Micron Patterns with Optical Lithography Using Bilayer Resist Compositions Comprising a Photosensitive Polysilsesquioxane
5,399,512	S. N. Mohammad, R. B. Renbeck, and K. M. Walter	Method of Making Carrier Conduction Conductor-Insulator Semiconductor (CIS) Transistor
5,399,516	A. S. Bergendahl, C. L. Bertin, J. E. Cronin, H. L. Kalter, D. M. Kenney, C. H. Lam, and H.-S. Lee	Method of Making Shadow RAM Cell Having a Shallow Trench EEPROM
5,399,902	H. R. Bickford, P. W. Coteus, and L. C. Matthew	Semiconductor Chip Packaging Structure Including a Ground Plane
5,400,028	E. S. Schlig	Charge Summing Digital to Analog Converter
5,400,051	L. A. Krueger, Jr.	Method and System for Generating Variably Scaled Digital Images
5,400,196	M. A. Moser, A. Sharma, and M. Sri-Jayantha	DASD with Spindle Imbalance Isolation and Method for Producing Same

5,400,235	L. B. Carroll	High Frequency Energy Saving DC to DC Power Converter
5,400,264	G. L. Phillis and J. A. Roecker	Suboptimal Joint Probabilistic Data Association
5,400,313	K. A. Belser, J. E. Hurst, Jr., and M. R. Madison	Optical Data Storage System and Method with Reduced Heat Buildup
5,400,323	J. F. Frenzel III and B. A. Youngman	Method for Controlling the Insertion of Stations into FDDI Network
5,400,326	D. D. Smith	Network Bridge
5,400,603	T. L. Bauer, W. A. Cavaliere, C. R. Dart II, T. H. Freebern, D. C. Linnell, J. M. Miller, and J. J. Wu	Heat Exchanger
5,400,640	R. L. Stuckey	Pyrotechnic Shock Machine
5,401,614	R. R. Dichiaro, J. T. Fahey, P. E. Jones, C. F. Lyons, W. M. Moreau, R. Sooriyakumaran, G. T. Spinillo, K. M. Welsh, and R. L. Wood	Mid and Deep-UV Antireflection Coatings and Methods for Use Thereof
5,401,677	R. D. Bailey, C. Cabral, Jr., B. Cunningham, H. M. Dalal, J. M. Harper, V. Sardesai, H. S. Wildman, and T. O. Williams	Method of Metal Silicide Formation in Integrated Circuit Devices
5,401,714	P. Chaudhari, C. A. Mueller, and H. P. Wolf	Field-Effect Device with a Superconducting Channel
5,401,909	H. Arnold, P. Lueck, G. Mohr, and T. ZurNieden	Printed Circuit Board with Locally Enhanced Wiring Density
5,401,911	H. Anderson, A. Bross, J. G. Cempa, R. O. Lussow, D. E. Myers, and T. J. Walsh	Via and Pad Structure for Thermoplastic Substrates and Method and Apparatus for Forming the Same
5,401,934	T. Ainsworth, Jr., G. Chiu, R. N. Singh, and J. S. Wilczynski	Lens System for Scanning Laser Apparatus
5,401,980	F. F. Fang and R. A. Webb	2D/1D Junction Device as a Coulomb Blockade Gate
5,402,052	D. C. Cheng, T. Makansi, and M. Sri-Jayantha	Inductive Current Switching System with Resonance
5,402,072	S. Chiang, H. W. Curtis, A. E. Falls, A. Halperin, J. P. Karidis, J. D. Mackay, D. C. Wong, K.-C. Woo, and L.-C. Zai	System and Method for Testing and Fault Isolation of High Density Passive Boards and Substrates
5,402,146	A. A. Rodriguez, M. A. Pietras, and A. S. Saenz	System and Method for Video Compression with Artifact Dispersion Control
5,402,147	I. Chen, T. A. Horvath, A. G. Lean, and B. C. Liang	Integrated Single Frame Buffer Memory for Storing Graphics and Video Data
5,402,149	A. Amagami and N. Aruga	Matrix Display Apparatus, Method and Circuit for Driving Same and Computer Having Same
5,402,308	I. Koyanagi, M. Ohtuki, F. Ohsawa, and T. Satoh	Portable Disk Storage Apparatus Using Flexible Cable for Shock Absorption
5,402,351	J. S. Batchelder, H. W. Curtis, D. S. Goodman, F. Gracer, R. R. Jackson, G. M. Koppelman, and J. D. Mackay	Model Generation System Having Closed-Loop Extrusion Nozzle Positioning
5,402,402	N. Kagami, H. Kubo, and M. Takekoshi	Apparatus for Controlling a Optical Disk Drive
5,402,416	R. A. Cieslak, C. J. Georgiou, and C.-S. Li	Method and System for Buffer Occupancy Reduction in Packet Switch Network
5,402,474	S. K. Miller, F. E. Mandato, and S. K. Gursahaney	System, Data Processing Method and Program to Provide a Programmable Interface Between a Workstation and an Archive Server to Automatically Store Telephone Transaction Information
5,402,532	D. A. Epstein, J. R. Rossignac, and J. W. Wu	Direct Display of CSG Expression by Use of Depth Buffers
5,402,801	R. H. Taylor	System and Method for Augmentation of Surgery
5,402,885	L. G. Cook, F. R. Krug, W. H. Lackner, and T. J. Walsh	Access Bank Container System
5,403,420	T. P. Gall, J. R. Loomis, D. B. Stone, C. L. Tytran, and J. Wilcox	Fabrication Tool and Method for Parallel Processor Structure and Package
5,403,619	J. J. Cuomo and J. E. Yehoda	Solid State Ionic Polishing of Diamond
5,403,682	B. J. Lin	Alternating Rim Phase-Shifting Mask
5,403,779	R. V. Joshi, J. J. Cuomo, H. M. Dalal, and L. L. Hsu	Refractory Metal Capped Low Resistivity Metal Conductor Lines and Vias Formed Using PVD and CVD
5,404,044	R. B. Booth, R. H. Gephart, B. S. Gremban, J. E. Poetzinger, and D. T. Shen	Parallel Process Interposer (PPI)
5,404,049	M. G. Canada, M. Nicewicz, J. R. Rawlins, and C. G. Rivadeneira	Fuse Blow Circuit
5,404,110	S. G. Golladay	System Using Induced Current for Contactless Testing of Wiring Networks
5,404,247	J. M. Cobb, M. J. LaPlante, D. C. Long, and F. Topolovec	Telecentric and Achromatic F-Theta Scan Lens System and Method of Use

5,404,254	M. Kisaka	Method and Apparatus for Controlling Head Position
5,404,372	A. Phillips, Jr.	Single Metal Level Laser Array
5,404,392	R. V. Miller, V. S. Moore, and T. K. Pate	Digital Cellular Overlay Network (DCON)
5,404,435	W. S. Rosenbaum	Non-Text Object Storage and Retrieval
5,404,446	R. J. Bowater, B. K. Aldred, and S. P. Woodman	Dual Buffer Video Display System for the Display of Asynchronous Irregular Frame Rate Video Data
5,404,448	A. D. Bowen, T. J. Ebberts, and R. L. Henderson	Multi-Pixel Access Memory System
5,404,452	W. F. Detschel, D. W. Norton, Jr., and R. C. Paddock	Personal Computer Bus and Video Adapter for High Performance Parallel Interface
5,404,461	H. T. Olnowich, R. F. Lusch, and J. D. Jabusch	Broadcast/Switching Apparatus for Executing Broadcast/Multicast Transfers Over Unbuffered Asynchronous Switching Networks
5,404,471	S. Kawano, H. Komiyama, and S. Mukohyama	Method and Apparatus for Switching Address Generation Modes in CPU Having Plural Address Generation Modes
5,404,500	V. J. Legvold and S. C. Cowley	Storage Control System with Improved System and Technique for Destaging Data from Nonvolatile Memory
5,404,504	D. J. Byers and D. E. Denning	Trace Tool for Serial, Optical Interface
5,404,522	D. E. Carmon, W. G. Crouse, and M. S. Ware	System for Constructing a Partitioned Queue of DMA Data Transfer Requests for Movements of Data Between a Host Processor and a Digital Signal Processor
5,404,524	J. Celi, Jr.	System for Identifying Attached Input Pointing Devices, Loading Associated Software Routines, and Interacting with Anyone Input Pointing Device While Disabling the Others
5,404,525	J. C. Endicott, S. J. Munroe, and R. P. Resch	Efficient Method Router that Supports Multiple Simultaneous Object Versions
5,404,532	W. C. Allen, J. L. Panian, and P. J. Reder	Persistent/Impervious Event Forwarding Discriminator
5,404,537	H. T. Olnowich and D. B. Kirk	Priority Interrupt Switching Apparatus for Real Time Systems
5,404,538	T. G. Krappweis, Sr.	Method and Apparatus for Multilevel Bus Arbitration
5,404,543	M. R. Faucher, C. M. Herring, and M. W. Kellogg	Method and System for Reducing an Amount of Power Utilized by Selecting a Lowest Power Mode from a Plurality of Power Modes
5,404,563	L. L. Green, P. H. Gum, R. E. Hough, S. L. Rankin, S. J. Schmandt, R. M. Smith, Sr., V. A. Spano, P. C. Yeh, and D. S. Yu	Scheduling Normally Interchangeable Facilities in Multiprocessor Computer Systems
5,405,069	R. R. Duncan and R. A. Felix	Paper Motion Detection System
5,405,795	K. D. Beyer, T. N. Buti, C.-M. Hsieh, L. L. Hsu	Method of Forming a SOI Transistor Having a Self-Aligned Body Contact
5,406,070	A. D. Edgar and S. C. Penn	Method and Apparatus for Scanning an Object and Correcting Image Data Using Concurrently Generated Illumination Data
5,406,197	S. J. Mercier and E. G. Vail III	Apparatus for Controlling Test Inputs of Circuits on an Electronic Module
5,406,289	T. N. Barker, J. A. Persichetti, A. B. Poore, Jr., and N. Rijavec	Method and System for Tracking Multiple Regional Objects
5,406,310	J. M. Aschenbrenner, G. E. Dewlen, and N. R. Pierman	Managing Color Selection in Computer Display Windows for Multiple Applications
5,406,515	T. V. Rajeevakumar	Method for Fabricating Low Leakage Substrate Plate Trench DRAM Cells and Devices Formed Thereby
5,406,529	H. Asano	Flash Non-Volatile Memory
5,406,640	G. Fitzpatrick, W. J. Johnson, R. S. Keller, and M. L. Williams	Method of and Apparatus for Producing Predominate and Non-Predominate Color Coded Characters for Optical Character Recognition
5,407,269	R. W. Sherry and D. W. Towne	Dynamic Mixing Chamber
5,407,349	R. R. Hansotte, Jr., D. K. Neff, D. A. Rock, J. A. Walker, and R. M. Wanser	Exhaust System for High Temperature Furnace
5,407,710	T. H. Baum, P. B. Comita, C. E. Larson, and G. W. Tyndall III	Laser Interconnection of Circuits on Transparent Substrate
5,408,098	O. C. Wells	Method and Apparatus for Detecting Low Loss Electrons in a Scanning Electron Microscope
5,408,192	J. A. Bailey	Monolithic Broadband Phase Shifting Circuit for Analog Data Signals
5,408,319	J.-M. Halbout	Optical Wavelength Demultiplexing Filter for Passing a Selected One of a Plurality of Optical Wavelengths
5,408,366	S. R. Bentley, R. W. Murray, S. M. Paranjape, and F. Quintana	Apparatus and Method for Detecting and Validating Formatted Blocks on Magnetic Tape
5,408,373	C. H. Bajorek, R. E. Fontana, C. D. Snyder, D. A. Thompson, M. L. Williams, and C. E. Yeack-Scranton	Integrated Transducer-Suspension Assembly for Vertical Recording
5,408,377	B. A. Gurney, H. Lefakis, O. U. Need III, S. S. P. Parkin, V. S. Speriosu, and D. R. Wilhoit	Magnetoresistive Sensor with Improved Ferromagnetic Sensing Layer and Magnetic Recording System Using the Sensor

5,408,409	E. Glassman, W. A. Hanson, P. Kazanzides, B. D. Mittelstadt, B. L. Musits, H. A. Paul, and R. H. Taylor	Image-Directed Robotic System for Precise Robotic Surgery Including Redundant Consistency Checking
5,408,528	D. G. Carlson and T. E. Miller	Method and Apparatus for Flexible and Optimal Telephone Call Acceptance and Routing
5,408,540	A. Zlotnick	Character Slant Recognition in a Word Image
5,408,587	F. Maier, C. Mueller, and S. Saelens	Expert System with a Frame Explanation System
5,408,594	W. J. Johnson, R. S. Keller, and M. L. Williams	Method and Apparatus for Visual Accentuation of Displayed Information
5,408,598	D. W. Pryor, Jr.	Method for Fast Generation of Parametric Curves Employing a Pre-Calculated Number of Line Segments in Accordance with a Determined Error Threshold
5,408,618	M. E. Aho, K. D. Cramer, and R. A. Diedrich	Automatic Configuration Mechanism
5,408,646	H. T. Olnowich and A. R. Williams	Multipath Torus Switching Apparatus
5,408,653	J. W. Josten, T. L. Masatani, C. Mohan, I. S. Narang and J. Z. Teng	Efficient Data Base Access Using A Shared Electronic Store in a Multi-System Environment with Shared Disks
5,408,656	O. Cohn, M. H. Hartung, W. F. Micka, K. M. Nagin, Y. Novick, N. Rotics, A. Winokur and E. Zeidner	Method and System for Non-Specific Address Data Retrieval in a Data Storage Subsystem Which Includes Multiple Datasets Stored at Specific Addresses
5,408,658	R. N. Rechtschaffen and K. Ekanadham	Self-Scheduling Parallel Computer System and Method
5,408,659	C. J. Cavendish and R. L. Baber	Link Pane Class and Application Framework
5,409,743	M. D. Bouffard, W. J. King, and C. M. Martin	PECVD Process for Forming BPSG with Low Flow Temperature
5,409,852	T. B. Faure, B. S. Meyerson, W. D. Pricer, and C. C. Smolinski	Method of Manufacturing Three Dimensional Integrated Device and Circuit Structures
5,409,853	A. J. Yu	Process of Making Silicided Contacts for Semiconductor Devices
5,410,186	A. R. Bonaccio, J. E. Gersbach, C. J. Goetschel, and S. L. Von Bruns	Programmable Digital to Analog Converter
5,410,233	W. D. Carbaugh, Jr., M. J. LaPlante, D. C. Long, K. F. Stroms, C. D. Setzer	Magneto-Repulsion Punching with Dynamic Damping
5,410,334	L. D. Comerford	Switch for Pen-Down Sensing in Electronic Styli
5,410,409	M. Ray	Search Routine for Ellipsometers
5,410,439	G. T. Egbert, H. H. Ottesen, and G. J. Smith	Disk File with Clearance and Glide Measurement and Early Head Crash Warning
5,410,514	H. Miyatake	Single Clock Memory Having a Page Mode
5,410,530	M. E. Best, H. J. Rosen, K. A. Rubin, and T. C. Strand	Multiple Data Surface Optical Data Storage System
5,410,536	V. V. Shah, I. D. Judd, B. J. Grainger, and G. J. Cockburn	Method of Error Recovery in a Data Communication System
5,410,560	Y. Taira	Wavelength Conversion Apparatus
5,410,648	D. P. Pazel	Debugging System Wherein Multiple Code Views are Simultaneously Managed
5,410,653	J. F. Macon, Jr., S. Ong, and F.-H. W. Shih	Asynchronous Read-Ahead Disk Caching Using Multiple Disk I/O Processes and Dynamically Variable Prefetch Length
5,410,654	D. J. Foster and A. Garcia	Interface with Address Decoder for Selectively Generating First and Second Address and Control Signals Respectively in Response to Received Address and Control Signals
5,410,655	J. D. Greenfield, M. J. Mitchell, Jr., and W. R. Taylor	Intersystem Channel Paging System Having a Circuit for Executing Synchronous or Asynchronous Instructions for Transferring Messages Between Devices and a Shared Storage
5,410,657	C. H. Olson and T. M. Potter	Method and System for High Speed Floating Point Exception Enabled Operation in a Multiscalar Processor System
5,410,663	R. A. Blackburn, K. N. Langston, and P. G. Sutton	Method and System for Cache Memory Congruence Class Management in a Data Processing System
5,410,679	P. G. Lubold, R. S. Manka, R. A. Niemitolo, and K. S. Porter	Method and Apparatus for Concurrently Supporting Multiple Levels of Keyboard Display Terminal Functionality on a Single Physical Input/Output Controller Interface in an Information Handling System
5,410,684	M. K. Ainsworth, C. C. Barnes, R. B. Bennett, and R. C. Will	Log Name Exchange for Recovery of Protected Resources
5,410,692	R. J. Torres	Method for Enhancing Interactive Query of a Database
5,410,695	J. A. Frey, A. A. Helffrich, J. M. Nick, and M. D. Swanson	Apparatus and Method for List Management in a Coupled Data Processing System
5,410,697	R. Baird, G. Eisenberger, A. S. Lett, J. J. Myers, W. H. Tetzlaff, and J. G. Unger	Concurrency Management Using Version Identification of Shared Data as a Supplement to Use of Locks

5,410,699	R. Bealkowski, J. W. Blackledge, Jr., D. S. Cronk, R. A. Dayan, G. D. Kovach, M. S. Palka, Jr., R. Sachsenmaier, and K. M. Zyvoloski	Apparatus and Method for Loading BIOS from a Diskette in a Personal Computer System
5,410,700	J. G. Fecteau, J. M. Gdaniec, J. P. Hennessy, J. F. MacDonald, and D. I. Osisek	Computer System which Supports Asynchronous Commitment of Data
5,410,702	R. L. Abraham, R. E. Moore, W. L. Rich, F. W. Shackelford, J. R. Tiller, Jr., and R. S. Briggs, Jr.	Slot Objects for an Object Oriented Programming System
5,410,727	R. S. Jaffe, H. Li, M. M. L. Kienzle, and M.-C. Sheng	Input/Output System for a Massively Parallel, Single Instruction, Multiple Data (SIMD) Computer Providing for the Simultaneous Transfer of Data Between a Host Computer Input/Output System and All SIMD Memory Devices
5,410,732	R. N. Ames and V. S. Moore	Method and Apparatus for Decentralizing Uplink Network Control Functions and Minimizing Terminal Power Level Assessment Overhead in Radio Frequency Data Communications Networks
5,410,807	A. Bross and T. J. Walsh	High Density Electronic Connector and Method of Assembly
5,411,358	G. Garric and A. Lafond	Dispatching Apparatus With a Gas Supply Distribution System for Handling and Storing Pressurized Sealable Transportable Containers
5,411,703	S. G. Gonya, J. K. Lake, R. C. Long, and R. N. Wild	Lead-Free, Tin, Antimony Bismuth, Copper Solder Alloy
5,411,905	A. Acovic and B. S. Wu	Method of Making Trench EEPROM Structure on SOI with Dual Channels
5,412,189	B. J. Cragun	Touch Screen Apparatus with Tactile Information
5,412,246	D. M. Dobuzinsky, D. L. Harmon, S. R. Kasi, D. M. Kenney, S. V. Nguyen, T. Nguyen, and P.-H. Pan	Low Temperature Plasma Oxidation Process
5,412,384	C.-C. Chang, G. L. Davoll, M. H. El-Ruby, C. A. Friske, B. R. Iyer, J. P. Lazarus, D. Wilhite, and K. E. Plambeck	Method and System for Adaptively Building a Static Ziv-Lempel Dictionary for Database Compression
5,412,518	J. A. Christner, E. A. Cunningham, G. J. Kerwin, and J. M. Poss	Individual MR Transducer Head/Disk/Channel Adaptive Bias Current System
5,412,519	D. C. Buettner, W. J. Rothschild, and G. J. Smith	Optimization of Disk Drive Spindle Speed During Low Power Mode
5,412,534	S. J. Cutts, D. Gaunt, I. Golledge, A. Hamper, E. Johnson, D. Newmarch, and J. Veal	Modular Housing
5,412,536	T. M. Anderson, G. M. Chrysler, R. C. Chu, and R. E. Simons	Local Condensation Control for Liquid Impingement Two-Phase Cooling
5,412,613	D. E. Galbi, M. P. Clinton, and M. W. Kellogg	Memory Device Having Asymmetrical CAS to Data Input/Output Mapping and Applications Thereof
5,412,637	N. Kagami, K. Nakase, and H. Kubo	Method for Putting an Optical Head in a Stationary State and Optical Disk Drive Apparatus
5,412,653	K. H. Hoppe, L. H. McGoogan, L. Skarshinski, and M. E. Underkoffler	Dynamic Switch Cascading System
5,412,654	C. E. Perkins	Highly Dynamic Destination-Sequenced Destination Vector Routing for Mobile Computers
5,412,661	H. T. Hao and S. W. Ng	Two-Dimensional Disk Array
5,412,665	A. J. Gruodis, P. C. Patel, and K. P. Szabo	Parallel Operation Linear Feedback Shift Register
5,412,668	D. W. Dewey	Parity Striping Feature for Optical Disks
5,412,689	B. K. Chan and T. R. Scott	Modal Propagation of Information Through a Defined Transmission Medium
5,412,723	R. Canetti and A. Herzberg	Mechanism for Keeping a Key Secret from Mobile Eavesdroppers
5,412,766	M. A. Pietras and A. A. Rodriguez	Data Processing Method and Apparatus for Converting Color Image Data to Non-Linear Palette
5,412,776	M. A. Bloomfield and C. A. H. Andrew	Method of Generating a Hierarchical Window List in a Graphical User Interface
5,412,784	R. N. Rechtschaffen and K. Ekanadham	Apparatus for Parallelizing Serial Instruction Sequences and Creating Entry Points into Parallelized Instruction Sequences at Places Other Than Beginning of Particular Parallelized Instruction Sequence
5,412,797	W. B. Rubin	Method for Implementing One-to-Many Binary Relations in Object-Oriented Systems Using Doubly-Linked Rings
5,412,803	N. G. Bartow, P. J. Brown, R. S. Capowski, L. T. Fasano, T. A. Gregg, G. Salyer, and D. W. Westcott	Communications System Having Plurality of Originator and Corresponding Recipient Buffers with Each Buffer Having Three Different Logical Areas for Transmitting Messages in Single Transfer
5,412,805	L. E. Jordan II, S.-B. Shi, M. J. Sirkin, and P. E. Stephens	Apparatus and Method for Efficiently Allocating Memory to Reconstruct a Data Structure

5,413,321	J. M. Banks, M. R. Clearman, Jr., J. C. Colson, D. O. Craig, D. C. Loose, and J. E. Varan	System and Method for Operating a Document Assembly System
5,413,835	Y. Ikeda and S. Takayama	Magnetic Recording Medium Having an Underlayer of Low Melting Point Metal Alloy in the Form of Spherically Shaped Structures
5,413,842	C. N. Felisberto, S. A. Milkovich, and R. W. Nufer	Green Sheets of Controlled Microporosity and Method of Producing Same
5,414,293	R. F. Broom	Encapsulated Light Emitting Diodes
5,414,303	S. G. Gonya, J. K. Lake, R. C. Long, and R. N. Wild	Lead-Free, High Tin, Ternary Solder Alloy of Tin, Bismuth, and Indium
5,414,382	T. R. Larson and R. S. Taylor	Impedance Buffer for Driving Capacitive Loads
5,414,423	W. B. Pennebaker	Stabilization of Probability Estimates by Conditioning on Prior Decisions of a Given Context
5,414,469	C. A. Gonzales, D. Ramm, and E. Viscito	Motion Video Compression System with Multiresolution Features
5,414,570	S. M. Fry, S. A. Jackson, and R. A. Ripberger	Tape Marks Recorded in User Data Blocks Having Identifications of the Recorded Tape Marks
5,414,574	Z. E. Boutaghoul, S. S. Malek, and D. A. Richards	Hybrid Base for Ultrathin Disk Drives
5,414,580	M. D. Levenson and H. A. E. Santini	Magnetic Storage System Using Thin Film Magnetic Recording Heads Using Phase-Shifting Mask
5,414,637	C. L. Bertin, C. P. Miller, and D. J. Perlman	Intra-Module Spare Routing for High Density Electronic Packages
5,414,731	E. Antunes, D. F. Bantz, F. J. Bauchot, E. D. Bello, M. Ferraz, F. LeMaut, and B. da Silva Araujo Tavares	Command Set and Procedure for Synchronization of Frequency Hopping Control Clocks
5,414,740	M. M. Denneau, B. D. Gavril, P. H. Hochschild, and C. B. Stunkel	Synchronous Communication System Having Multiplexed Information Transfer and Transition Phases
5,414,797	S. Vassiliadis, G. Triantafyllos, and W. Kobrosly	Clustering Fuzzy Expected Value System
5,414,805	R. D. Gordon, J. F. Sinnott, Jr., and L. S. Walling	Visual Display Transition Effects Using Sorted Table of Display Cells
5,414,806	J. J. C. Richards	Palette and Parts View of a Composite Object in an Object Oriented Computer System
5,414,808	M. L. Williams	Method for Accessing and Manipulating Library Video Segments
5,414,810	M. T. Doyle and J. Kane	Method and Apparatus for Maintaining Data Integrity When Displaying Multiple Dialog Windows
5,414,812	M. J. Filip, K. L. Karunungan, J. C. Kramer, L. C. Lee, D. L. Moore, C. C. Shih, and J. J. Sydir	System for Using Object-Oriented Hierarchical Representation to Implement a Configuration Database for a Layered Computer Network Communications Subsystem
5,414,832	M. M. Denneau, B. D. Gavril, P. H. Hochschild, and C. B. Stunkel	Tunable Synchronous Electronic Communication Apparatus
5,414,833	P. C. Hershey, D. B. Johnson, A. V. Le, S. M. Matyas, J. G. Wacławsky, and J. D. Wilkins	Network Security System and Method Using a Parallel Finite State Machine Adaptive Active Monitor and Responder
5,414,834	V. I. Alexander, J. L. Gaudet, L. E. Jordan II, and R. Hernandez	Method and Apparatus for Data Storage and Interchange Using a Relational Database Table, and a Data Record for Use in Connection Therewith
5,414,836	W. J. Baer and P. C. Leung	Software Testing System That Employs a Graphical Interface to Generate Test Cases Configured as Hybrid Tree Structures
5,414,841	R. E. Bingham, M. J. Dockter, J. F. Farber, and K. D. Seppi	Computerized System for Representing Data Items Using Token Identifiers
5,414,842	G. D. Archer, D. R. Case and H. J. Wu	External Sorting Using Virtual Storage as a Work Device
5,414,844	D. S. Wang	Method and System for Controlling Public Access to a Plurality of Data Objects Within a Data Processing System
5,414,845	J. L. Behm, G. Balakrishnan, and D. G. Eisenhauer	Network-Based Computer System with Improved Network Scheduling System
5,414,846	P. Lemble, G. Menanteau, S. Pacchiano, G. Sagols, and A. Truchi	Method of Facilitating the Configuration of Programs
5,414,851	F. W. Brice, Jr., J. C. Elliott, K. J. Fredericks, R. E. Galbraith, M. J. Halma, R. E. Hough, S. M. John, P. A. Malinowski, A. S. Meritt, K. J. Oakes, J. C. Rathjen, Jr., M. W. Sachs, D. E. Stucki, and L. W. Wyman	Method and Means for Sharing I/O Resources by a Plurality of Operating Systems
5,414,852	P. H. Kramer and K. A. Tate	Method for Protecting Data in a Computer System
5,414,853	P. Fertig and I. Golan	Apparatus and Method for Checking Microcode with a Generated Restriction Checker
5,414,855	W. West	Language Compiler